**A**

**MINI PROJECT REPORT ON**

# “Quiz Application System”

**Submitted By**

Mr.BEDAGE SUSHANT SUGRIV [Roll No. 06]

Mr.PAWAR PRATIK SADASHIV[Roll No.22]

Mr.PATIL RAKESH ANILKUMAR[Roll No.64]

In the partial fulfillment of the requirement of

Bachelor of Technology in Computer Science and Engineering of

Dr. Babasaheb Ambedkar Technological University, Lonere

**Under The Guidance of**

Prof. D.V.Jadhav



Shri Pandurang Pratishthan Pandharpur’s

## **Karmayogi Institute of Technology, Shelve, Pandharpur**

**Accredited by NAAC ‘B++’ Grade**

**2023-24**

Shri Pandurang Pratishthan Pandharpur’s

**Karmayogi Institute of Technology, Shelve, Pandharpur**

**Accredited by NAAC ‘B++’ Grade**



# Certificate

This is to certify that,

**Mr. BEDAGE SUSHANT SUGRIV [Roll No. 06]**

**Mr.** **PAWAR PRATIK SADASHIV[Roll No.22]**

**Mr. PATIL RAKESH ANILKUMAR[Roll No.64]**

of T**.**Y**. [**Computer Science and Engineering] have satisfactorily and successfully completed their mini project entitled as, **“Quiz Application System”** and have submitted this mini project report in partial fulfillment of the requirements for the degree of “Bachelor of Technology [Computer Science and Engineering]” of the Dr. Babasaheb Ambedkar Technological University, Lonere during academic year 2023-24.

Date:

|  |  |
| --- | --- |
| **Prof. D.V.Jadhav** |  |
| **Project Guide** | **External** |
| **Prof. D. V. Bhosale** | **Dr. S. P. Patil** |
| **HoD[CSE]** | **Principal** |

|  |  |  |  |
| --- | --- | --- | --- |
| **CONTENTS** | | |  |
|  | | | Page  NO |
| Acknowledgement | | | I |
| Abstract | | | II |
| List of Figures | | | VI |
| List of Tables | | | VIII |
|  | | |  |
| Chapter No. |  | |  |
| 1 | INTRODUCTION | | 01 |
|  | 1.1 | Need | 01 |
|  | 1.2 | Objectives | 03 |
|  | 1.3 | Proposed Work | 03 |
|  |  | |  |
| 2 | SYSTEM DESIGN | |  |
|  | 2.1 | Block Diagram | 04 |
|  | 2.2 | Circuit Diagram | 07 |
|  | 2.3 | Circuit Simulation | 08 |
| 3 | THEORETICAL BACKGROUND | |  |
|  | 3.1 | Working | 07 |
|  | 3.2 | Advantages | 08 |
|  | 3.3 | Disadvantages |  |
| 4 | conclusion | |  |
|  | 4.1 | Conclusion | 11 |
|  | 4.2 | Future Scope | 11 |
| 5 | References | |  |
|  | 5.1Referance | | 12 |

## Acknowledgement

At this pleasing moment of having partially completed our project, we wish to convey our sincere thanks and gratitude to the management of our college who provide all facilities to us.

With deep sense gratitude, we extend our earnest & sincere thanks to our guide Prof D.V.Jadhav sir Department of CSE Engineering for his kind guidance & encouragement during this project.

First and fore most our sincere thanks to the principal Prof. Dr.S.P.Patil sir for forwarding us to do our project and offering adequate duration in partially completing our project.

We are also grateful to Head of the department of CSE Prof. D.V.Bhosale for his constructive suggestion and encouragement during our project.

## Abstract